

AMENDMENT TO SPECIFICATION

Please add the following to the list of drawings on page 3:

Figure 6 is a block diagram of a multiple error correcting code (ECC) mechanism for use with transactions with a computer system, according to one embodiment of the present invention.

Please add the following new paragraph to the specification.

Please place the following new paragraph between the last and second to last paragraphs on page 8 of the specification.

Figure 6 is a block diagram of a multiple error correcting code (ECC) mechanism for use with transactions with a computer system, according to one embodiment of the present invention. The multiple ECC mechanism 600 includes an ECC encoder 610 and a decoder 620. The ECC encoder 610 can apply a first ECC code to a first portion of a transaction and a second ECC code to a second portion of the transaction, for example according to various embodiments described herein. The first portion of the transaction can be a header packet and the second portion of the transaction can be one or more data packets, according to various embodiments described herein. The decoder 620 can decode the first and the second ECC codes, for example according to various embodiments described herein.